Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S2	92	@ad<"20050511" and (voltage with limit\$3) same (subassembl\$3 (sub adj assembl\$3))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/22 09:59
S3	78	@ad<"20021125" and (voltage with limit\$3) same (subassembl\$3 (sub adjassembl\$3))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/22 10:05
S4	130057	@ad<"20021125" and (series with connect\$3 with (power voltage))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR .	ON	2007/03/22 10:07
S6	2	"5703790".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/22 10:12
S 7	4	("5703790").URPN.	USPAT	OR	ON	2007/03/22 10:13
S9	1	("5703790").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2007/03/22 10:14
S8 .	2	("5877928").URPN.	USPAT	OR	ON	2007/03/22 10:14
S10	- 11	("4870570" "5021679" "5142684" "5153535" "5265256" "5307003" "5311392" "5317366" "5523978" "5525913" "5530932").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2007/03/23 10:37
S12	567	S11 and ("327"/\$4.ccls. 716/6.ccls.)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON -	2007/03/23 10:53

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S15	10	S14 and "327"/\$4.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/23 10:58
S14	287	@ad<"20021125" and ((shift\$3 adjust\$3 chang\$3 alter\$3 modif\$7) with (clock "clk" "ck")) and (series with connect\$3 with (processor))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/23 10:58
S11	2885	@ad<"20021125" and ((shift\$3 adjust\$3 chang\$3 alter\$3 modif\$7) with (clock "clk" "ck")) and (diode) and (transistor with series)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/23 11:05
S16	504	@ad<"20021125" and (single with power with series with (processor transistor device) with (connect\$3))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/24 11:00
S20	8	(("5703790") or ("5486783") or ("5894508") or ("6262493") or ("6308279") or ("4730268") or ("5877928") or ("6229360")).PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/04/24 11:23
S19	1	("5014189").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/04/24 11:23
S21	4	("5703790").URPN.	USPAT	OR	ON	2007/04/24 11:32
S22	49580	@ad<"20021125" and (series with connect\$3 with (power voltage supply) with (terminal connection port))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/24 11:34
S5	49565	@ad<"20021125" and (series with connect\$3 with (power voltage supply) with (terminal connection port))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/24 11:34

S23 ·	1195	S22 and ((shift\$3 adjust\$3 chang\$3 alter\$3 modif\$7) with (clock "clk" "ck")) and (series with connect\$3 with (transistor processor))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/24 11:36
S13	4649	@ad<"20021125" and ((shift\$3 adjust\$3 chang\$3 alter\$3 modif\$7) with (clock "clk" "ck")) and (series with connect\$3 with (transistor processor))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/24 11:36
S24	63309	@ad<"20021125" and ((processor transistor) with series with (interconnect\$3 connect\$3 (inter adj connect\$3)))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/24 11:37
S25	438	S23 and ("327"/\$4.ccls. or "716"/\$2. ccls. or "326"/\$4.ccls.)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/24 11:38
S18	35	S17 and ("327"/\$4.ccls.)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/24 11:38
S17	2692	@ad<"20021125" and (processor with series with (interconnect\$3 connect\$3 (inter adj connect\$3)))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/24 13:58
S29	1885	@ad<"20021125" and (synch\$9 with processor and (series with processor))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/24 14:45

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S28	591	@ad<"20021125" and (synch\$9 with processor same series)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/24 14:45
S27	247	@ad<"20021125" and (synch\$9 with processor with series)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/24 14:46
S30	50	(S27 or S28 or S29) and ("716"/\$2. ccls. "326"/\$4.ccls. "327"/\$4.ccls.)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/24 14:47
L2	578	@ad<"20021125" and ((shift\$4 decoupl\$3 ("de" adj coupl\$3) ("by" adj pass\$3) bypass\$3) near3 capacitor) and ((cascad\$3 series) same (connect\$3 interconnect\$3 (inter adj connect\$3)) same (processor\$3 cpu transistor) same (supply power)) and ((level voltage) near4 (shift\$3))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/25 10:23
L3	35	@ad<"20021125" and ((shift\$4 decoupl\$3 ("de" adj coupl\$3) ("by" adj pass\$3) bypass\$3) near3 capacitor) and ((cascad\$3 series) same (connect\$3 interconnect\$3 (inter adj connect\$3)) same (clock "ck" "clk") same (processor\$3 cpu transistor) same (supply power)) and ((level voltage) near4 (shift\$3))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/25 11:25
L1	1085	@ad<"20021125" and ((shift\$4 decoupl\$3 ("de" adj coupl\$3) ("by" adj pass\$3) bypass\$3) near3 capacitor) and ((cascad\$3 series) same (connect\$3 interconnect\$3 (inter adj connect\$3)) same (supply power)) and ((level voltage) near4 (shift\$3))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/25 14:08
L21	74	@ad<"20021125" and (dielectric adj3 (zone)) same (insulat\$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/25 14:09

L20	732	@ad<"20021125" and (dielectric adj3 (zone area)) same (insulat\$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/25 14:21
L23	251	@ad<"20021125" and (silicon adj "on" adj insulator)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/25 14:22
L22	251	@ad<"20021125" and (silicon near "on" near insulator)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/25 14:22
L24	18076	@ad<"20021125" and (silicon adj2 insulator)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/25 14:24
L25	2403	@ad<"20021125" and (silicon adj2 insulator) with (benefit advantage typical known conventional)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/25 14:29
L26	627	@ad<"20021125" and (silicon adj2 insulator) with (benefit advantage)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/25 14:30